

Seat No.: _____

Enrolment No. _____

GUJARAT TECHNOLOGICAL UNIVERSITY

B. E. Sem. - V - Examination – June- 2011

Subject code: 150701

Subject Name: Advance Processors

Date: 20/06/2011

Time: 10:30 am – 01:00 pm

Total Marks: 70

Instructions:

1. Attempt all questions.
2. Make suitable assumptions wherever necessary.
3. Figures to the right indicate full marks.

- Q.1** (a) State the function of components of 8086 internal architecture. **07**
(b) Answer the following. **07**
(i) Explain features of Pentium-pro architecture.
(ii) Describe the difference between the instructions
MOV AX, 2437H and MOV AX, [2437H]
- Q.2** (a) Explain coding template for 8086 instructions which MOV data between register or between a register and a memory location. State example. **07**
(b) Write a program for multiplication of two numbers. **07**
OR
(b) Write a program for division of a word by a byte. **07**
- Q.3** (a) Explain 8086 pin functions. **07**
(b) Write a program for to find factorial of given number. **07**
OR
- Q.3** (a) Explain 80286 architecture. **07**
(b) Write a program to generate Fibonacci series. **07**
- Q.4** (a) Explain Paging and Segmentation. **07**
(b) Explain XLAT/XLATB, EQU and DW. **07**
OR
- Q.4** (a) Explain Task scheduling. **07**
(b) Describe Hyper threading. **07**
- Q.5** (a) Explain features of RISC architecture. **07**
(b) Describe the purpose of the 8086 interrupt vector table. **07**
OR
- Q.5** (a) Explain features of CISC architecture. **07**
(b) Describe the main use of the 8086 type 1 interrupt. **07**
