Seat No.:	Enrolment No
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Subject code: 160903

GUJARAT TECHNOLOGICAL UNIVERSITY

B. E. - SEMESTER - VI • EXAMINATION - WINTER 2012

Date: 04/01/2013

Subj	ect l	Name: Microcontroller	
Time	e: 02	.30 pm - 05.00 pm Total Marks: 70	
Instr	uct	ions:	
		Attempt any five questions.	
	2.	Make suitable assumptions wherever necessary.	
	3.	Figures to the right indicate full marks.	
Q.1	(a)	Draw and explain the Architectural block diagram of 8051.	08
Ų.1	(b)	· · · · · · · · · · · · · · · · · · ·	06
Q.2	(a)	•	07
		mentioning the all necessary details.	
	(b)	1 1	07
		bit addressable in this region. Also explain function of SP, DPTR and PC.	
	(h)	OR List the various general purpose registers available in 8051 microcontroller. Also	07
	(0)	mention where these register are situated in memory map with their addresses. Give the	U1
		way of selection for any one register bank out of four register banks	
Q.3	(a)		07
		and PSEN pins.	
	(b)	Explain various jump instruction of 8051 microcontroller. Also mention their range.	07
0.2	()	OR	0=
Q.3	(a)	1	07
	(b)	detail. Explain the Interrupt facility of 8051 microcontroller using IE and IP register. Also	07
	(0)	mention the internal priority of interrupts and their vector locations.	07
Q.4	(a)		07
		to registers and vice versa (2)From external memory to 8051 microcontroller and vice	
		versa and (3) from program memory to 8051 microcontroller and vice versa	
	(b)	Write an ALP to count the number of 1's and 0's from the 8 bit data stored in memory	07
		location F000h. Store the result in F001h and F002h.	
ΩA	(a)	OR What are the assembler directives? Explain any two directives with suitable example,	07
Q.4	(a)	Also mention the Data types in 8051 C programming.	U/
Q.4	(b)		07
ζ	()	delay.	
Q.5	(a)	Draw and explain the Timer/counter control logic diagram for the selection of any one	07
		operation. Also mention the physical availability (situation) of each input signal and its	
		importance.	
	(b)	1 0 11	07
		microcontroller. OR	
Q.5	(a)		07
V	(a)	microcontroller.	07
	(b)	Draw and explain complete scheme to interface in 8 bit ADC to 8051 Microcontroller	07
	` '	using port 1 and port 2.	
