	Seat N	No.: Enrolment No	
		GUJARAT TECHNOLOGICAL UNIVERSITY M. E SEMESTER – II • EXAMINATION – WINTER • 2014	
	Subi	ect code: 1720107 Date: 05-12-2014	
	-	ect Name: Embedded System	
	-	e: 02:30 pm - 05:00 pm	
	Insti	ructions:	
		 Attempt all questions. Make suitable assumptions wherever necessary. Figures to the right indicate full marks. 	
Q.1	(a) (b)		07 07
Q.2	(a) (b)	Given 1 X 10 ⁵ Hz crystal controlled oscillator and 64 bit and any number of 16 bit terminal count timers, design a real-time clock that outputs the date and time down to milliseconds.	07 07
	(b)	OR Explain in detail 8051 register banks and stack	07
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Q.3	(a) (b)		07 07
	(~)	OR	
Q.3	(a)	Attempt following: (i) What are the functional requirements of embedded system? (ii) Give some examples for sophisticated embedded systems.	07
	(b)		07
Q.4	(a)	Write a C program to count in BCD from 00 to 99.(use the 2 digit BCD decoded 7 segment display as the output.	12
	(b)	List difference between DIP switch and PORT OR	02
Q.4	(a) (b)	Explain working of all important functional unit of Keypad Controllers	07 07
Q.5	(a)	Use the eight independent LEDS.	12
	(b)		02
Q.5	(a) (b)		07 07
